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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Toshiro HAYAKAWA, et al.

Application No.: 09/315,068

Confirmation No.: To be Assigned

Group Art Unit: 2881

Filed: May 20, 1999

Examiner: Jeffrey N. Zahn

For: SEMICONDUCTOR LASER AND METHOD OF MANUFACTURING THE SAME

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated February 13, 2001, please amend the above-identified application as follows:

**IN THE SPECIFICATION:**

**Page 5, replace the second full paragraph (lines 12-18) as follows:**

Accordingly, a method in which etching is carried out up to a portion immediately above the active layer has been generally employed. Figure 2 shows a ridge waveguide type laser having an n-side electrode 20, an n-GaAs substrate 11, an n-GaAs buffer layer 12, an n-AlGaAs cladding layer 13, an undoped SCH active layer 14, a p-AlGaAs cladding layer 16, a p-GaAs capping layer 17, a SiO<sub>2</sub> insulating film 18, and a p-side electrode 19. In this case, etching is carried out so that the upper cladding layer 16 is left in a small thickness 15 (about 0.1 to 0.3μm) by controlling the etching time.